

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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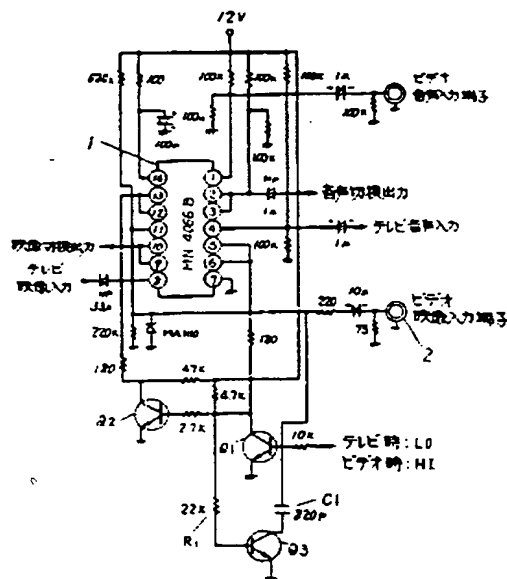
APPLICATION DATE : 19-05-86  
APPLICATION NUMBER : 61113951

APPLICANT : MATSUSHITA ELECTRIC IND CO LTD;

INVENTOR : NAKAO CHISATO;

INT.CL. : H04N 5/44 H04N 5/93

TITLE : VIDEO CROSSTALK REDUCTION  
CIRCUIT



ABSTRACT : PURPOSE: To decrease video crosstalk to be introduced to a television video signal by providing a serial circuit of a switching element and a capacitor to be electrified when a television video signal is selected, and attenuating a video signal inputted to the input terminal of a changing-over circuit at the time of receiving a television video signal.

CONSTITUTION: First, at the time of selecting a television, the base of a transistor Q1 comes to be a low level, the transistor Q1 is turned off and a transistor Q2 comes to be an ON condition. The No.11 pin of an ICI for changing-over is earthed through a transistor Q3 by a capacitor C<sub>1</sub>, the high frequency component of a video signal is attenuated and the interference to the television video signal is decreased. Next, at the time of selecting the video, the base of the transistor Q1 comes to be the high level, the transistor Q1 is turned on and the transistor Q2 comes to be the off condition. Simultaneously, the transistor Q3 comes to be the off condition and the video signal is not influenced by the capacitor C<sub>1</sub>. For such a reason, the video signal inputted from the No.11 pin is outputted from Nos. 9 and 10 pins of the IC 1 for changing-over.

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